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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/087,416
Filing Date February 27, 2002
Inventor Jigish D. Trivedi, et al.
Assignee Micron Technology, Inc.
Group Art Unit 2811
Examiner D.W. Owens
Attorney's Docket No. MI22-1965
Title: MOS Transistors With Nitrogen in the Gate Oxide of the P-Channel
Transistor

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

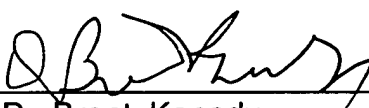
References -See Attached Form PTO-1449

The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449, copies of which are attached. No admission is made regarding whether all the submitted references are prior art.

Citation of the referenced art is respectfully requested.

Respectfully submitted,

Dated: 5-14-03

By: 
D. Brent Kenady
Reg. No. 40,045

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Application Serial No.	10/087,416
Filing Date	February 27, 2002
Inventor	Jigish D. Trivedi, et al.
Assignee	Micron Technology, Inc.
Group Art Unit	2811
Examiner	Not Yet Assigned
Attorney's Docket No.	MI22-1965
Title: Integrated Circuitry	



SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

Assistant Commissioner for Patents
Washington, D.C. 20231

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REMARKS

The Examiner's attention is directed to the reference which is listed on the attached Form PTO-1449 and a copy of which is attached. No admission is made regarding whether all the submitted references are prior art.


Citation of this patent is respectfully requested.

This Information Disclosure Statement is being filed within three months of the filing date of the application or before the mailing of a first Office Action, whichever occurs last. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this information disclosure statement, please charge the fee specified under 37

C.F.R. §1.17(p) to Deposit Account No. 23-0925. Please credit Deposit Account No. 23-0925 with any overpayment of the above fee.

Respectfully submitted,

Date: 5-6-02

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